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Page : 2 of 11

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Amendments to the Specification:

Please replace the abstract at page 35 with the following amended abstract:

Methods and apparatus, including computer program products, for microinstruction pointer stack in a processor. A method executed in [[a]] the processor includes executing microcode (μ code) [[stored]] pointed to in an out-of-order microinstruction pointer (μ IP) stack and manipulating the μ IP stack with a set of microinstructions.